



Fujitsu achieves wireless transmission speeds of 56 Gbps

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Fujitsu Laboratories and the Tokyo Institute of Technology developed a CMOS wireless transceiver chip that can process signals at high speeds with little loss across a broad range of frequencies, from 72 to 100 GHz. The new chip is part of an effort to further enlarge the capacity of wireless equipment. They also developed technology to modularise it. With these developments, they succeeded in achieving wireless transmission speeds of 56 Gbps, which Fujitsu said are the world's fastest. This technology makes it possible to have high-capacity wireless communications equipment that can be installed outdoors in applications where fibre-optic networks would be difficult to lay, Fujitsu explained.

A 56 Gbps transceiver circuit of the chip was developed by Fujitsu Laboratories together with Socionext. Fujitsu said it's the world's lowest power-consuming transceiver circuit that achieves communications speeds of 56 Gbps per channel. The 56 Gbps transceiver circuit achieves twice the speed as before without raising power consumption, Fujitsu said. This technology is able to increase the speed of data transfers between chips and optical modules without increasing power consumption, so it is expected to lead to performance improvements in next-generation servers and switches.



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