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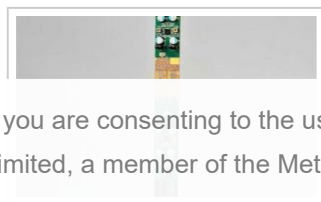
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[By David Manners](#) [4th June 2019](#)

## Tokyo Tech and NEC researchers demo 39GHz 5G transceiver

Researchers at Tokyo Institute of Technology (Tokyo Tech) and NEC Corporation have demonstrated a 39 GHz transceiver that could be used in the next wave of 5G wireless equipment, including base stations, smartphones, tablets and IoT applications.



Although research groups, including

the current team, have until now

largely focused on developing 28 GHz

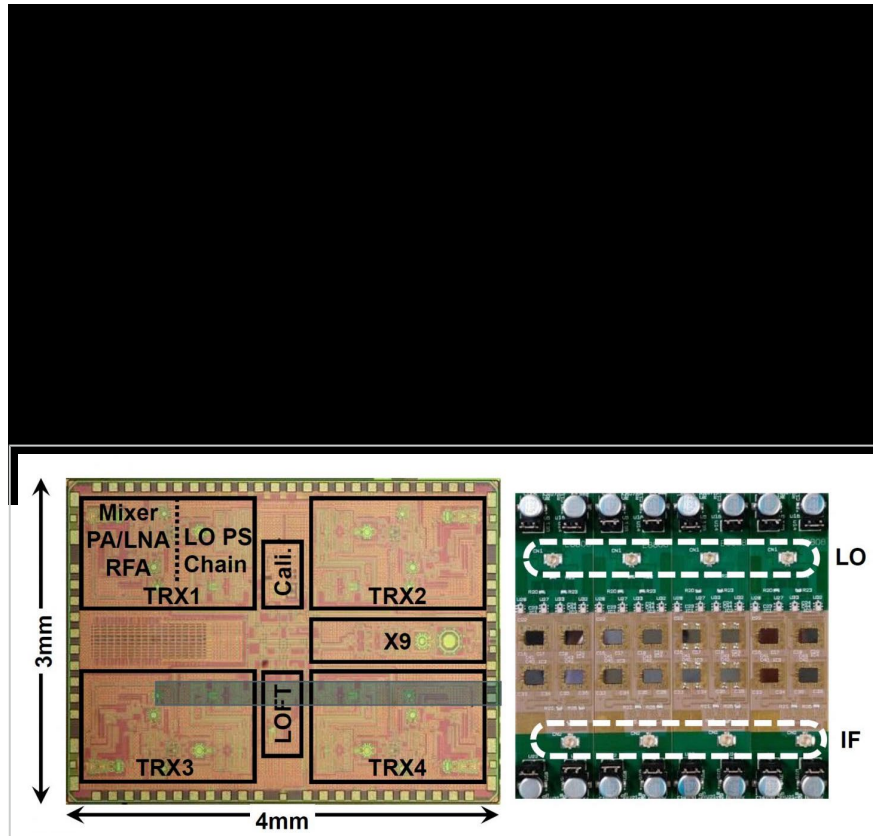
systems, 39 GHz will be another

important frequency band for realizing 5G in many parts of the world.

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The new transceiver (shown in Figure 1) is based on a 64-element (4 x 16) phased-array(1) design. Its built-in gain phase calibration means that it can improve beamforming(2) accuracy, and thereby reduce undesired radiation and boost signal strength.

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**Figure 1. A micrograph of the chip and the 64-element module**

The transceiver, based on a 64-element phased-array design, takes up a chip area of 12 mm<sup>2</sup>

Fabricated in a standard 65-nanometer CMOS process, the transceiver's low-cost silicon-based components make it ideal for mass production — a key consideration for accelerated deployment of 5G technologies.

The researchers showed that the built-in calibration has a very low root-mean-square (RMS) phase error of 0.08 degrees. This figure is an order of magnitude lower than previous

comparable results. While transceivers developed to date typically suffer from high gain variation of more than 1 dB, the new model has a maximum gain variation of just 0.04 dB over the full 360 degrees tuning range.

“We were surprised to achieve such a low gain variation when actually using the calibration based on our local-oscillator (LO) phase-shifting approach,” says project leader, Kenichi Okada of Tokyo Tech.

In addition, the transceiver has a maximum equivalent isotropic radiated power (EIRP)<sup>(4)</sup> of 53 dBm. This is an impressive indication of the output power of the 64 antennas, the researchers say, particularly for low-cost CMOS implementation.

Indoor testing (under anechoic chamber conditions<sup>(5)</sup>), which involved a one-meter, over-the-air measurement, demonstrated that the transceiver supports wireless transmission of a 400 MHz signal with 64QAM.

“By increasing the array scale, we can achieve greater communication distance,” Okada says. “The challenge will be to develop the transceiver for use in smartphones and base stations for 5G and beyond.”

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